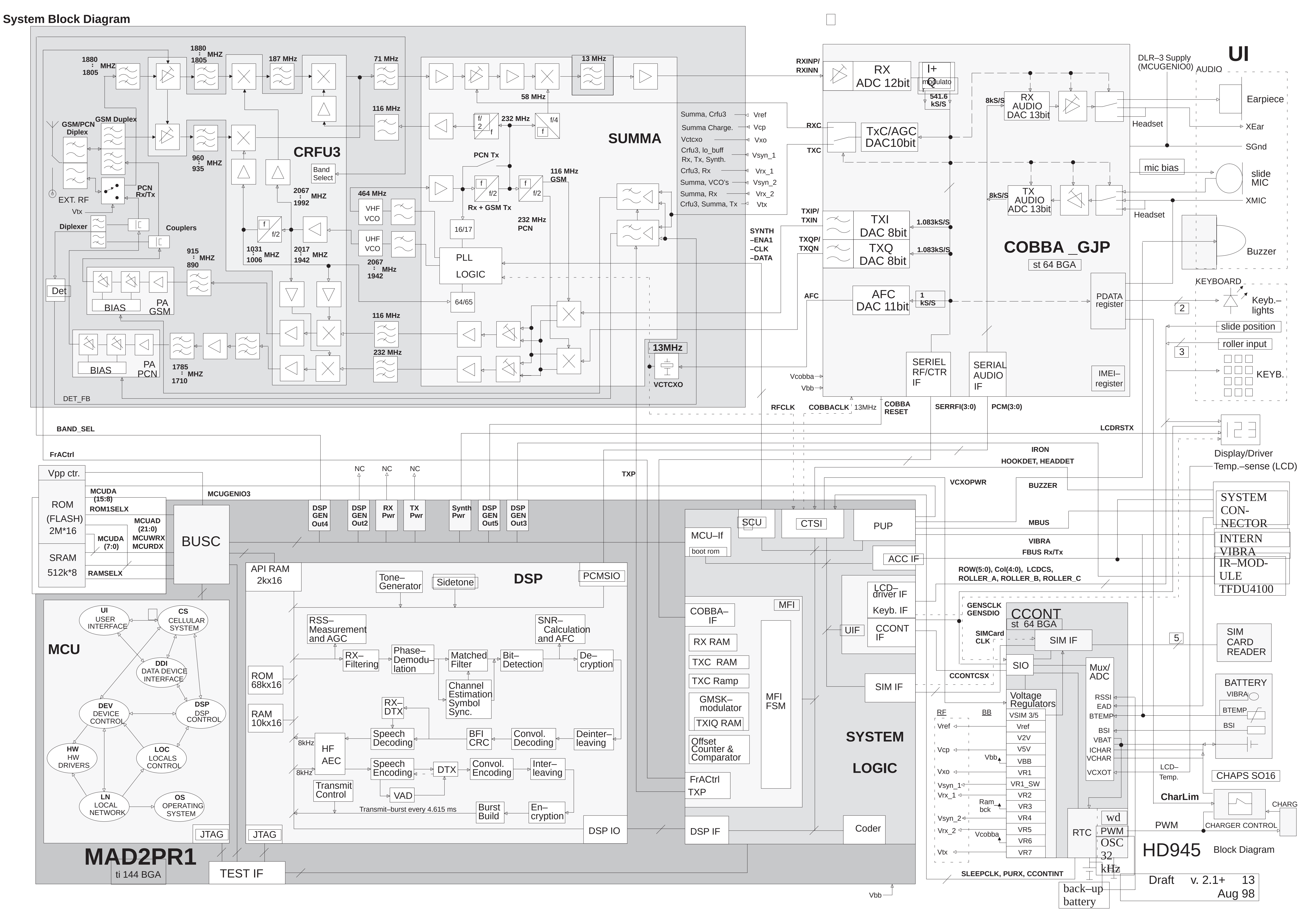
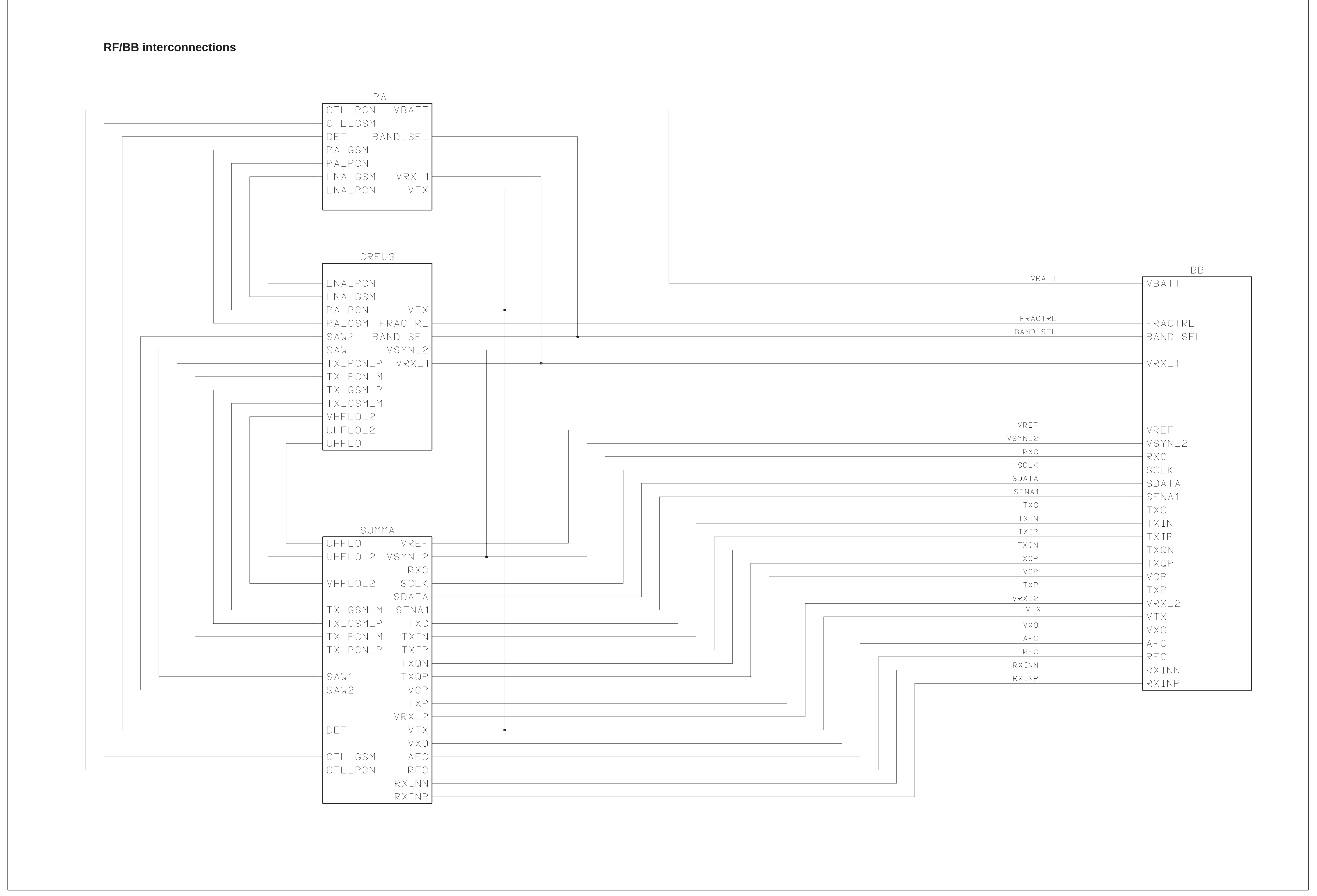
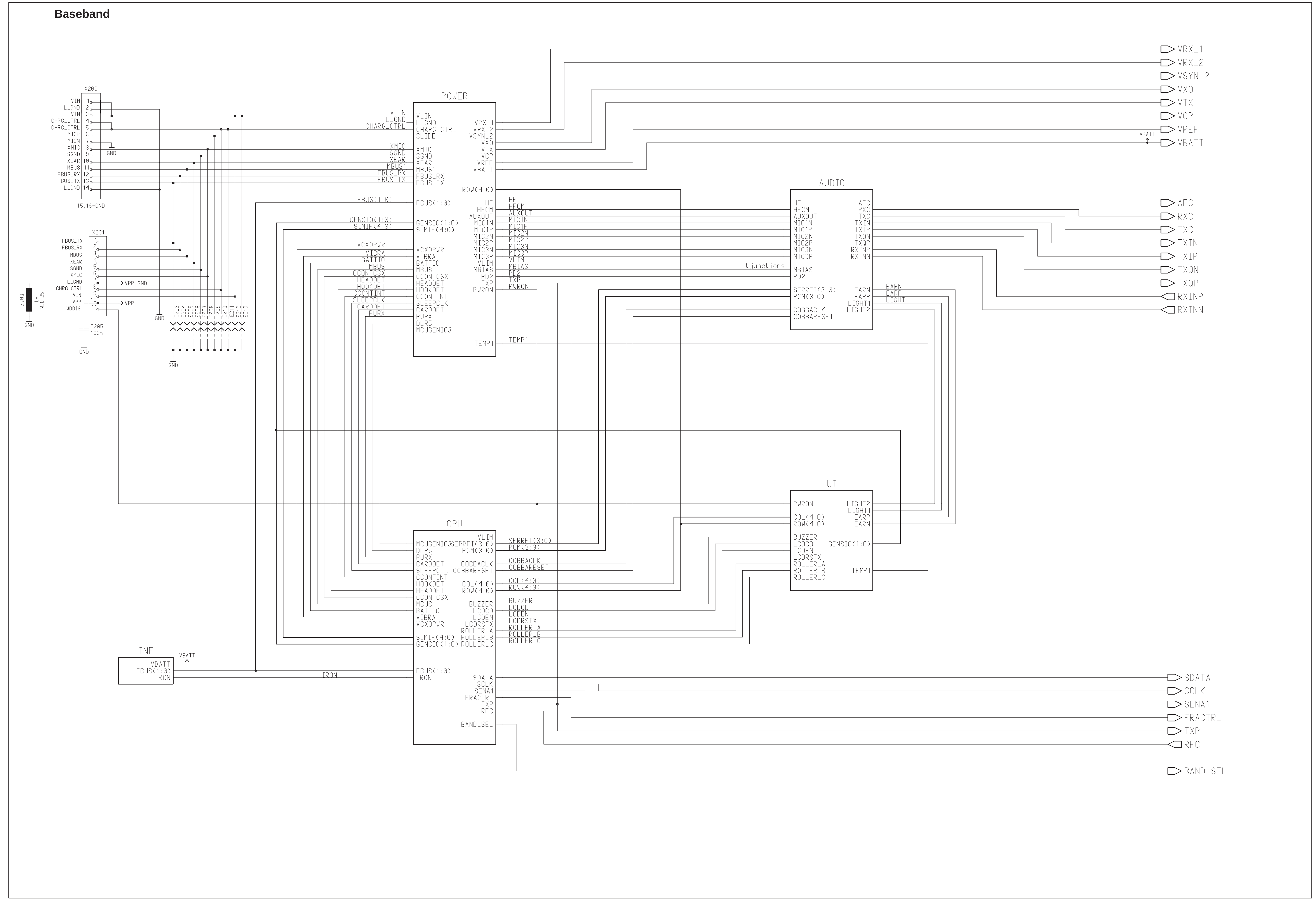
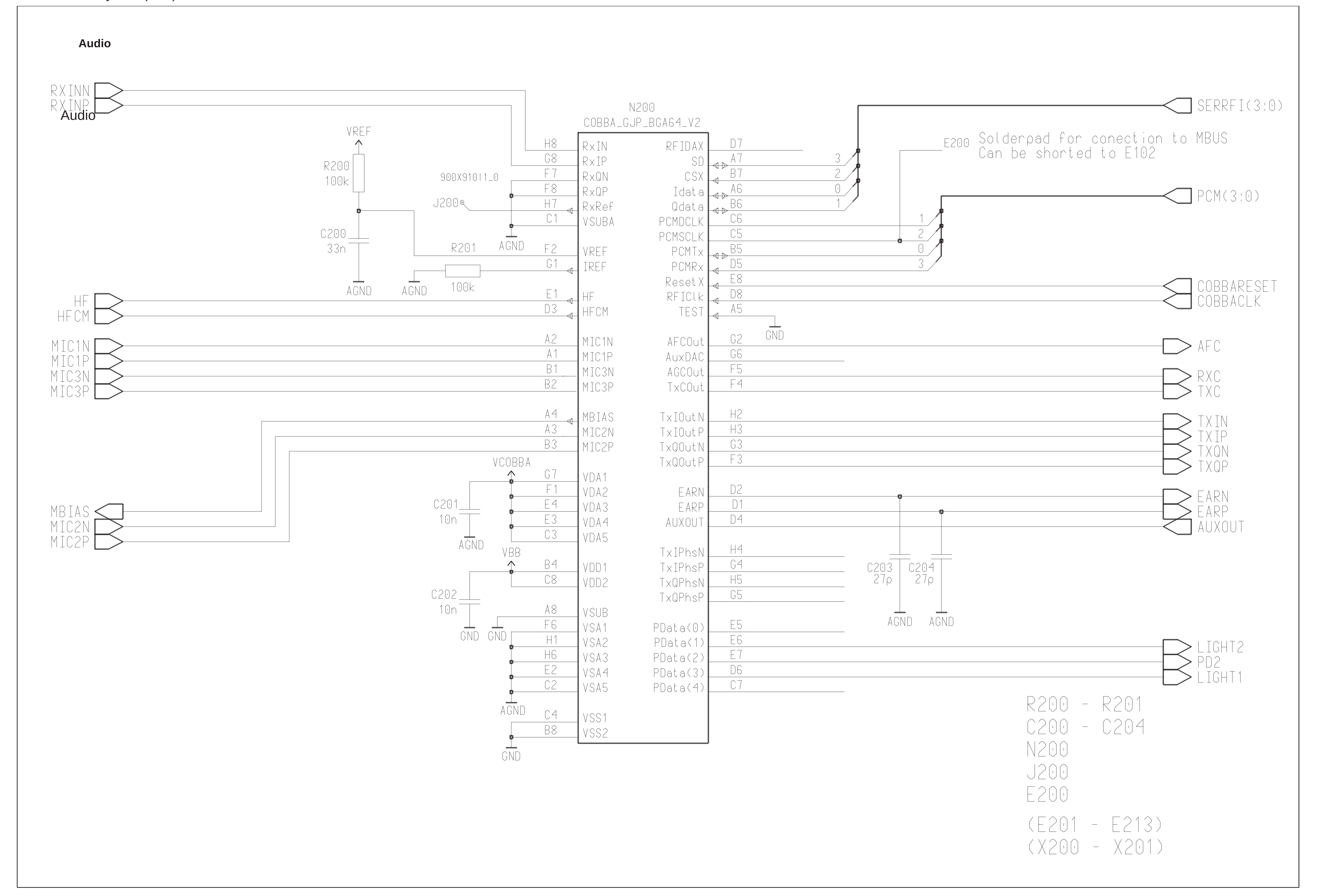
System Block Diagram

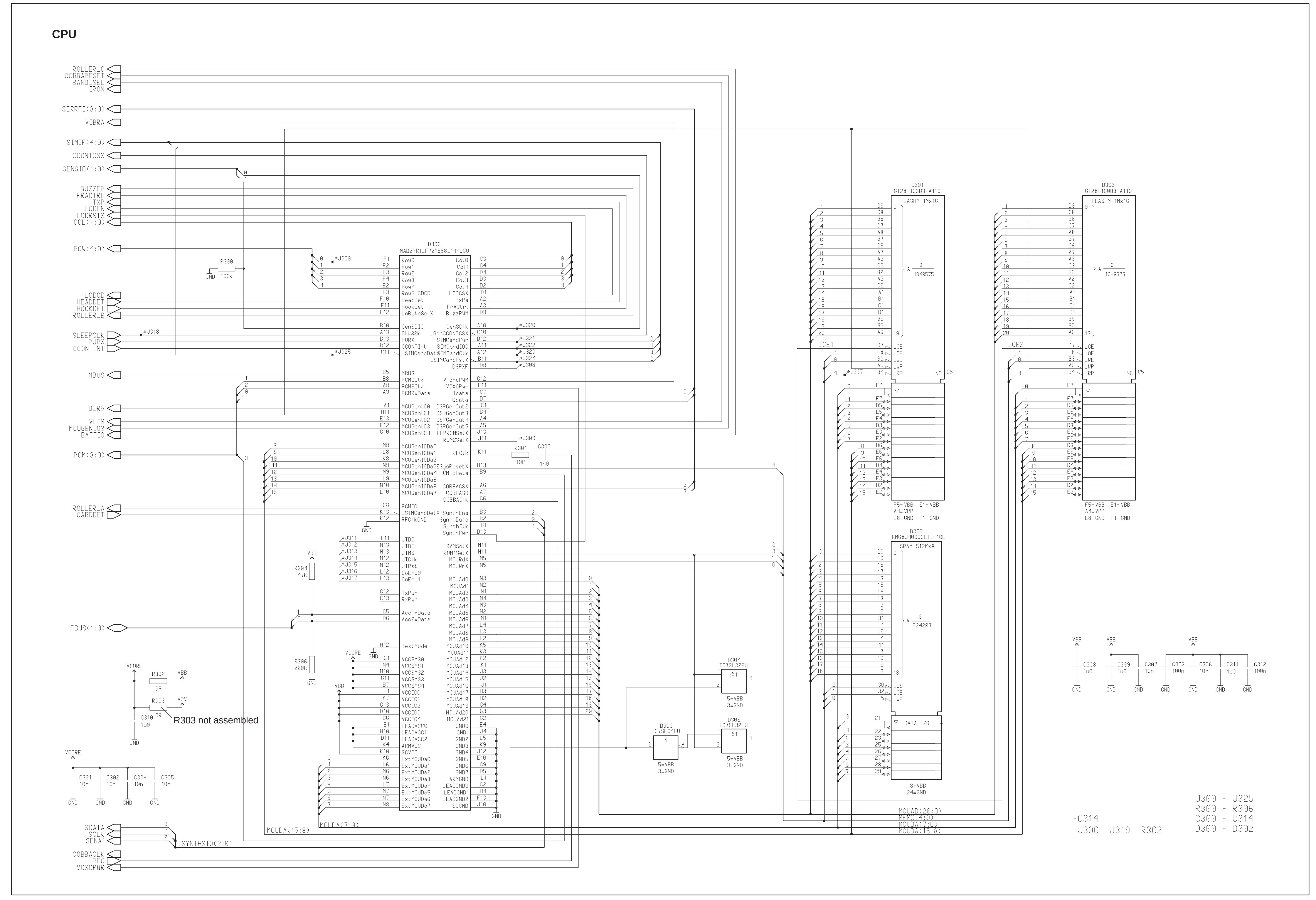



RF/BB interconnections
PA
CTL_PCN
CTL_GSM
DET
PA_GSM
PA_PCN
LNA_GSM
LNA_PCN
VBATT
BAND_SEL
VRX_1
VTX
CRFU3
LNA_PCN
LNA_GSM
PA_PCN
PA_GSM
SAW2
SAW1
TX_PCN_P
TX_PCN_M
TX_GSM_P
TX_GSM_M
VHFLO_2
UHFLO_2
UHFLO
VTX
FRACTRL
BAND_SEL
VSYN_2
VRX_1
SUMMA
UHFLO
UHFLO_2
VHFLO_2
TX_GSM_M
TX_GSM_P
TX_PCN_M
TX_PCN_P
SAW1
SAW2
DET
CTL_GSM
CTL_PCN
VREF
VSYN_2
RXC
SCLK
SDATA
SENA1
TXC
TXIN
TXIP
TXQN
TXQP
VCP
TXP
VRX_2
VTX
VXO
AFC
RFC
RXINN
RXINP
BB
VBATT
FRACTRL
BAND_SEL
VRX_1
VREF
VSYN_2
RXC
SCLK
SDATA
SENA1
TXC
TXIN
TXIP
TXQN
TXQP
VCP
TXP
VRX_2
VTX
VXO
AFC
RFC
RXINN
RXINP

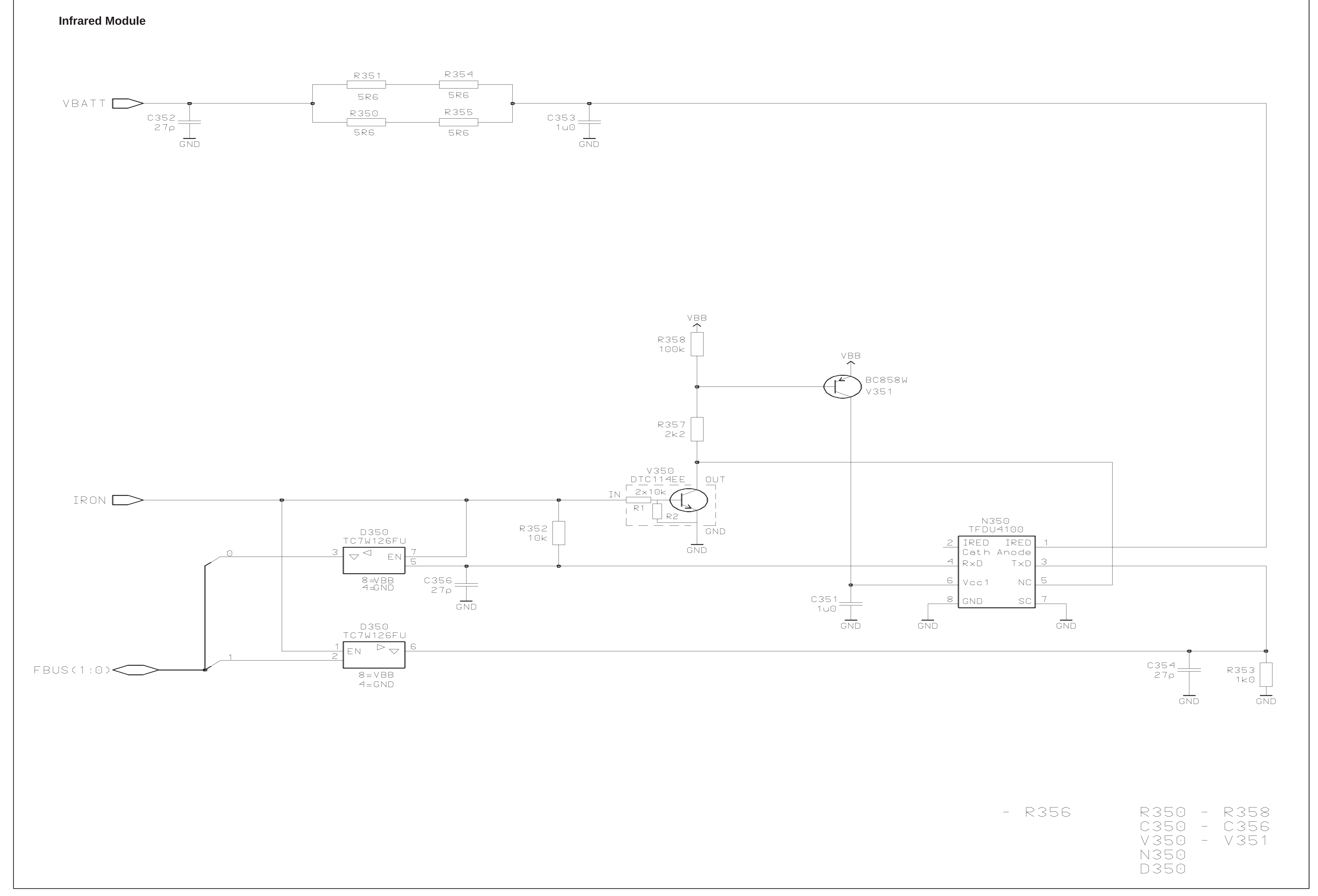
Baseband

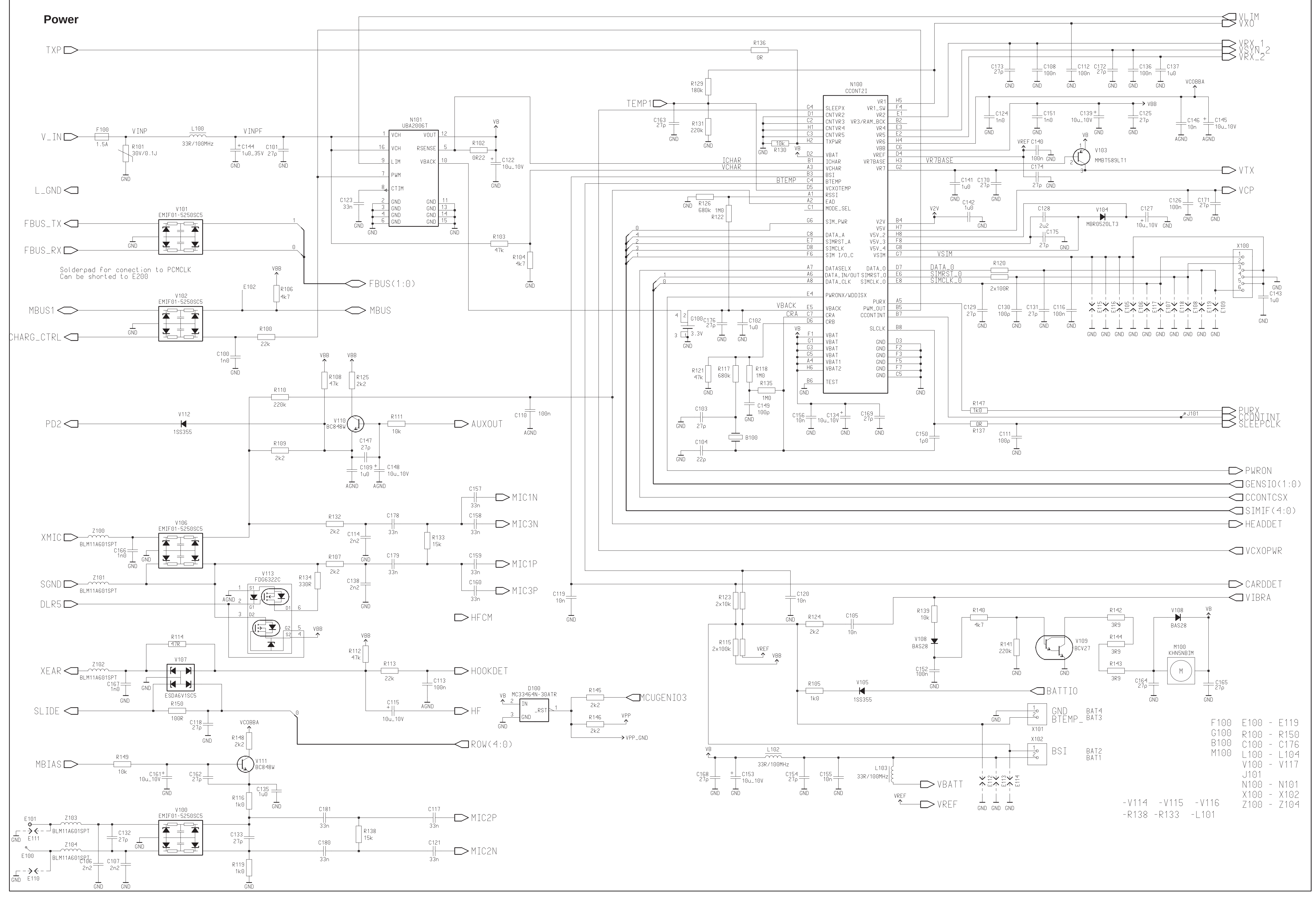

POWER
AUDIO
UI
CPU
INF
X200
X201
VRX_1
VRX_2
VSYN_2
VXO
VTX
VCP
VREF
VBATT
AFC
RXC
TXC
TXIN
TXIP
TXQN
TXQP
RXINP
RXINN
SDATA
SCLK
SENA1
FRACTRL
TXP
RFC
BAND_SEL


Audio
RXINN
RXINP
Audio
N200
COBBA_GJP_BGA64_V2
SERRFI(3:0)
E200 Solderpad for conection to MBUS
Can be shorted to E102
PCM(3:0)
VREF
R200
100k
C200
33n
R201
100k
900X910I1_0
J200
AGND
RxIN
RxIP
RxQN
RxQP
RxRef
VSUBA
VREF
IREF
HF
HFCM
MIC1N
MIC1P
MIC3N
MIC3P
MBIAS
MIC2N
MIC2P
VDA1
VDA2
VDA3
VDA4
VDA5
VDD1
VDD2
VSUB
VSA1
VSA2
VSA3
VSA4
VSA5
VSS1
VSS2
RFIDAX
SD
CSX
Idata
Qdata
PCMDCLK
PCMSCLK
PCMTx
PCMRx
ResetX
RFIClk
TEST
AFCOut
AuxDAC
AGCOut
TxCOut
TxIOutN
TxIOutP
TxQOutN
TxQOutP
EARN
EARP
AUXOUT
TxIPhsN
TxIPhsP
TxQPhsN
TxQPhsP
PData(0)
PData(1)
PData(2)
PData(3)
PData(4)
HF
HFCM
MIC1N
MIC1P
MIC3N
MIC3P
MBIAS
MIC2N
MIC2P
VCOBBA
C201
10n
VBB
C202
10n
GND
COBBARESET
COBBACLK
AFC
RXC
TXC
TXIN
TXIP
TXQN
TXQP
EARN
EARP
AUXOUT
C203
27p
C204
27p
LIGHT2
PD2
LIGHT1
R200 - R201
C200 - C204
N200
J200
E200
(E201 - E213)
(X200 - X201)


CPU
D300
MAD2PR1_F721558_144GGU
D301
GT28F160B3TA110
FLASHM 1Mx16
D303
GT28F160B3TA110
FLASHM 1Mx16
D302
KM68U4000CLTI-10L
SRAM 512Kx8
D304
TC7SL32FU
D305
TC7SL32FU
D306
TC7SL04FU
R303 not assembled
MCUAD(20:0)
MEMC(4:0)
MCUDA(7:0)
MCUDA(15:8)
SYNTHSIO(2:0)
-C314
-J306 -J319 -R302
J300 - J325
R300 - R306
C300 - C314
D300 - D302

Infrared Module



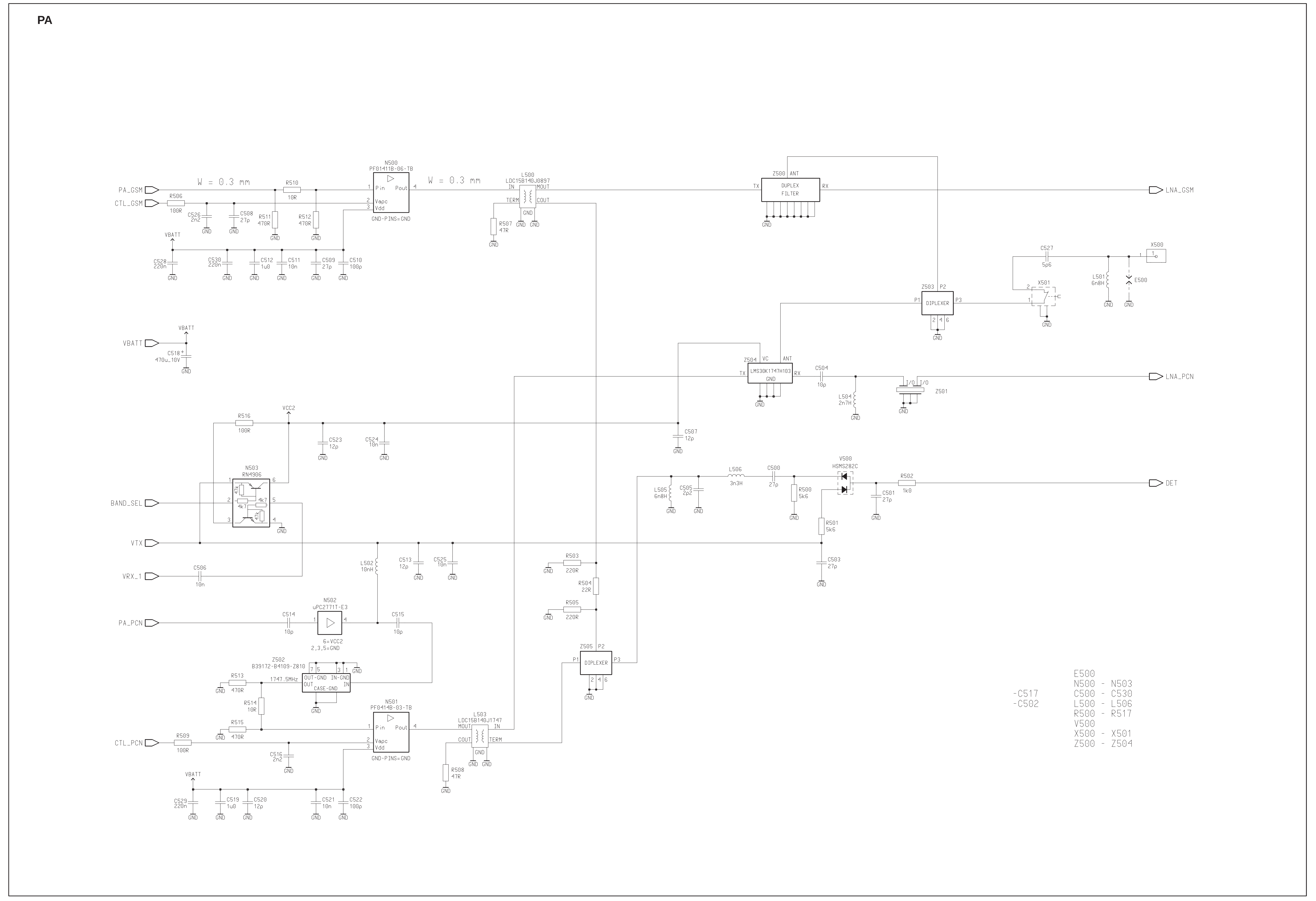

Power
TXP
V_IN
L_GND
FBUS_TX
FBUS_RX
MBUS1
CHARG_CTRL
PD2
XMIC
SGND
DLR5
XEAR
SLIDE
MBIAS
TEMP1
FBUS(1:0)
MBUS
AUXOUT
MIC1N
MIC3N
MIC1P
MIC3P
HFCM
HOOKDET
HF
ROW(4:0)
MIC2P
MIC2N
MCUGENIO3
VPP
VPP_GND
BATTIO
VBATT
VREF
VLIM
VXO
VRX_1
VSYN_2
VRX_2
VTX
VCP
PURX
CCONTINT
SLEEPCLK
PWRON
GENSIO(1:0)
CCONTCSX
SIMIF(4:0)
HEADDET
VCXOPWR
CARDDET
VIBRA
Solderpad for conection to PCMCLK
Can be shorted to E200
N100 CCONT2I
N101 UBA2006T
F100 E100 - E119
G100 R100 - R150
B100 C100 - C176
M100 L100 - L104
V100 - V117
J101
N100 - N101
X100 - X102
Z100 - Z104
-V114 -V115 -V116
-R138 -R133 -L101

CRFU3

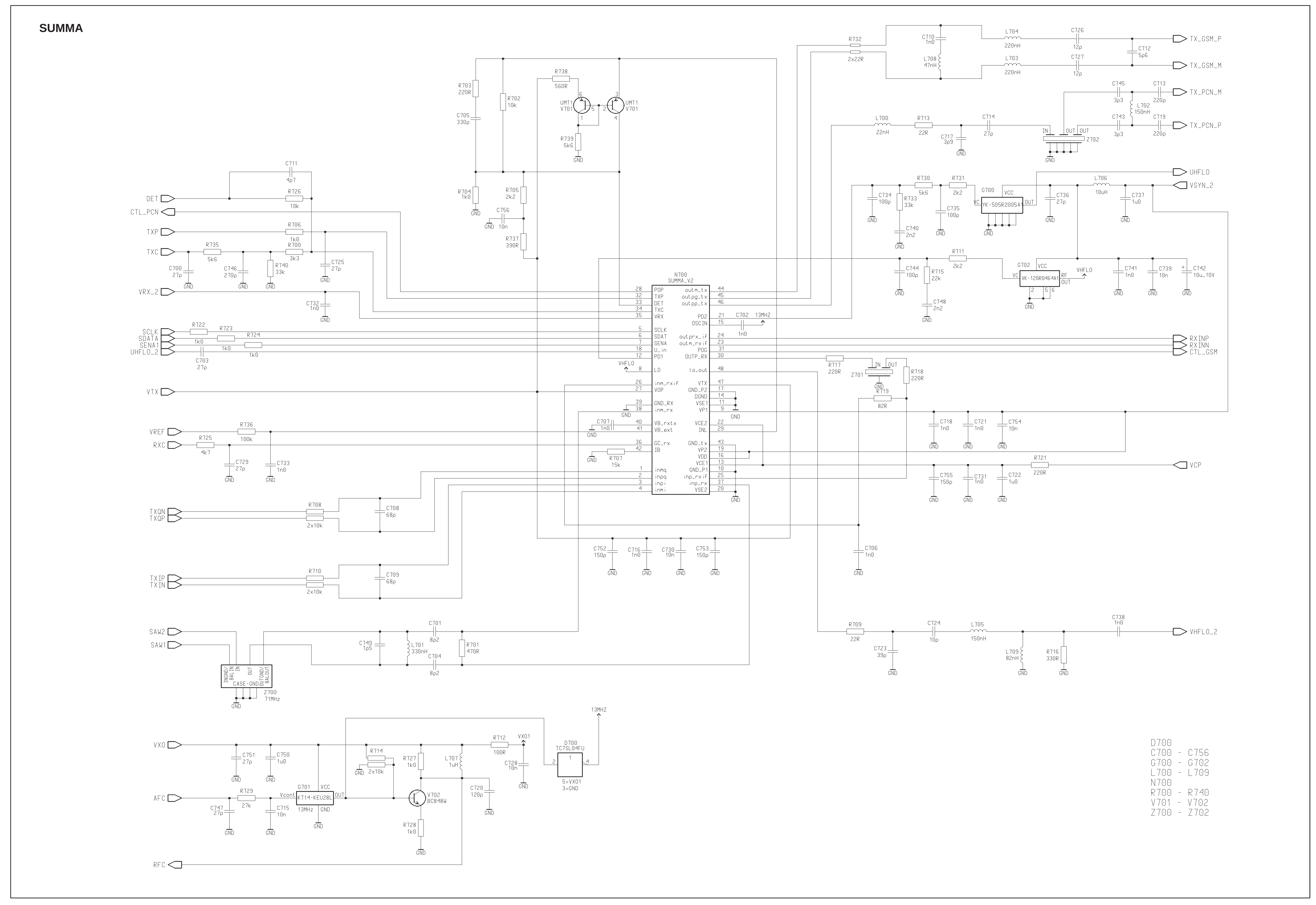


-C644 -C620 C600 - C647
L600 - L617
N600
-R617 -R613 R601 - R619
Z600 - Z605
T600
(A600 - A602)

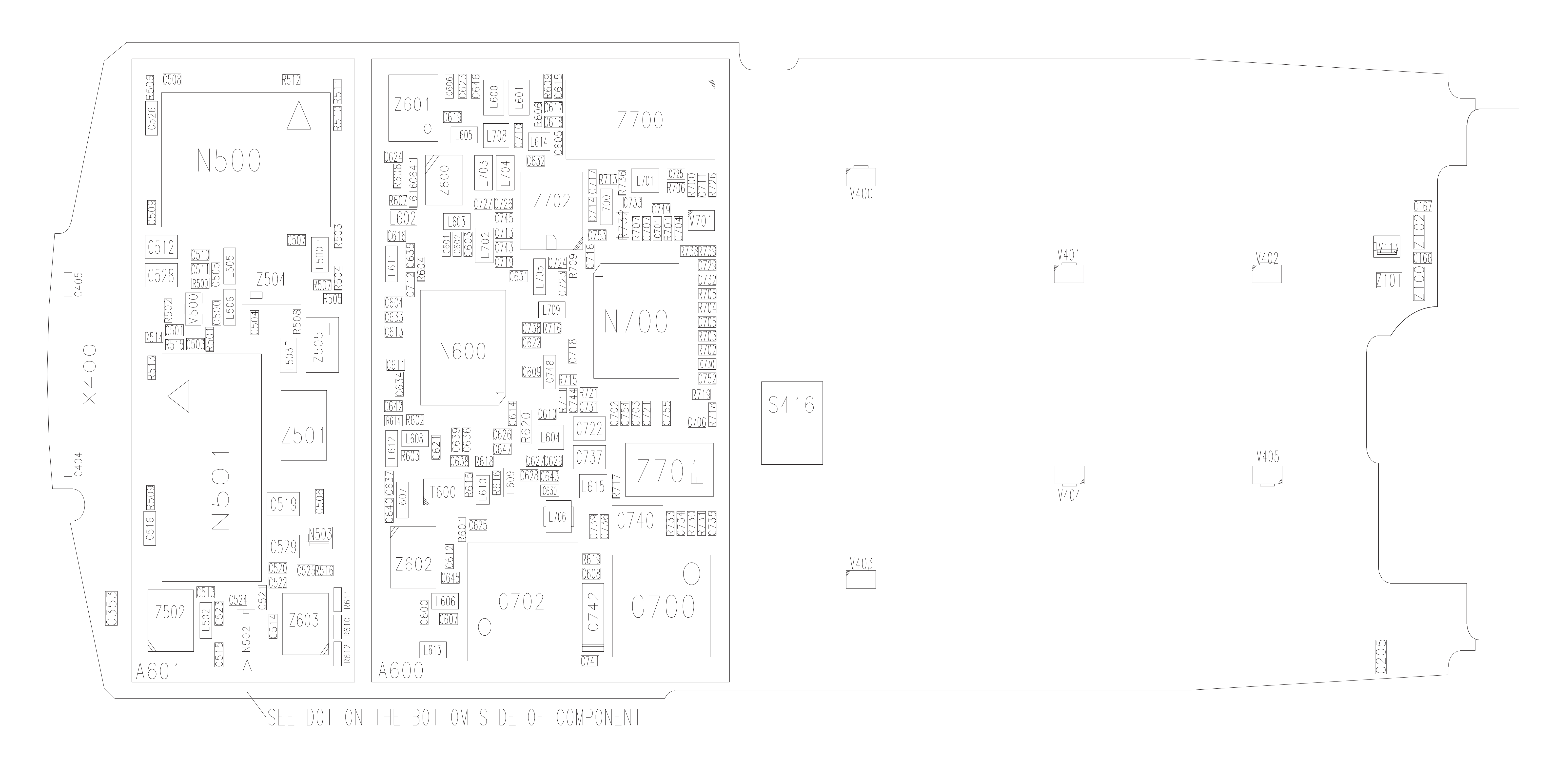
PA


PA_GSM
CTL_GSM
VBATT
BAND_SEL
VTX
VRX_1
PA_PCN
CTL_PCN
LNA_GSM
LNA_PCN
DET
W = 0.3 mm
N500
PF01411B-06-TB
L500
LDC15B140J0897
Z500
DUPLEX FILTER
Z503
DIPLEXER
Z504
LMS30K1747H103
Z501
X500
X501
E500
V500
HSMS282C
N503
RN4906
N502
uPC2771T-E3
6=VCC2
2,3,5=GND
Z502
B39172-B4109-Z810
1747.5MHz
N501
PF0414B-03-TB
L503
LDC15B140J1747
Z505
DIPLEXER
GND-PINS=GND
E500
N500 - N503
-C517 C500 - C530
-C502 L500 - L506
R500 - R517
V500
X500 - X501
Z500 - Z504

SUMMA



Component Layout – Top



Component Layout –Bottom

